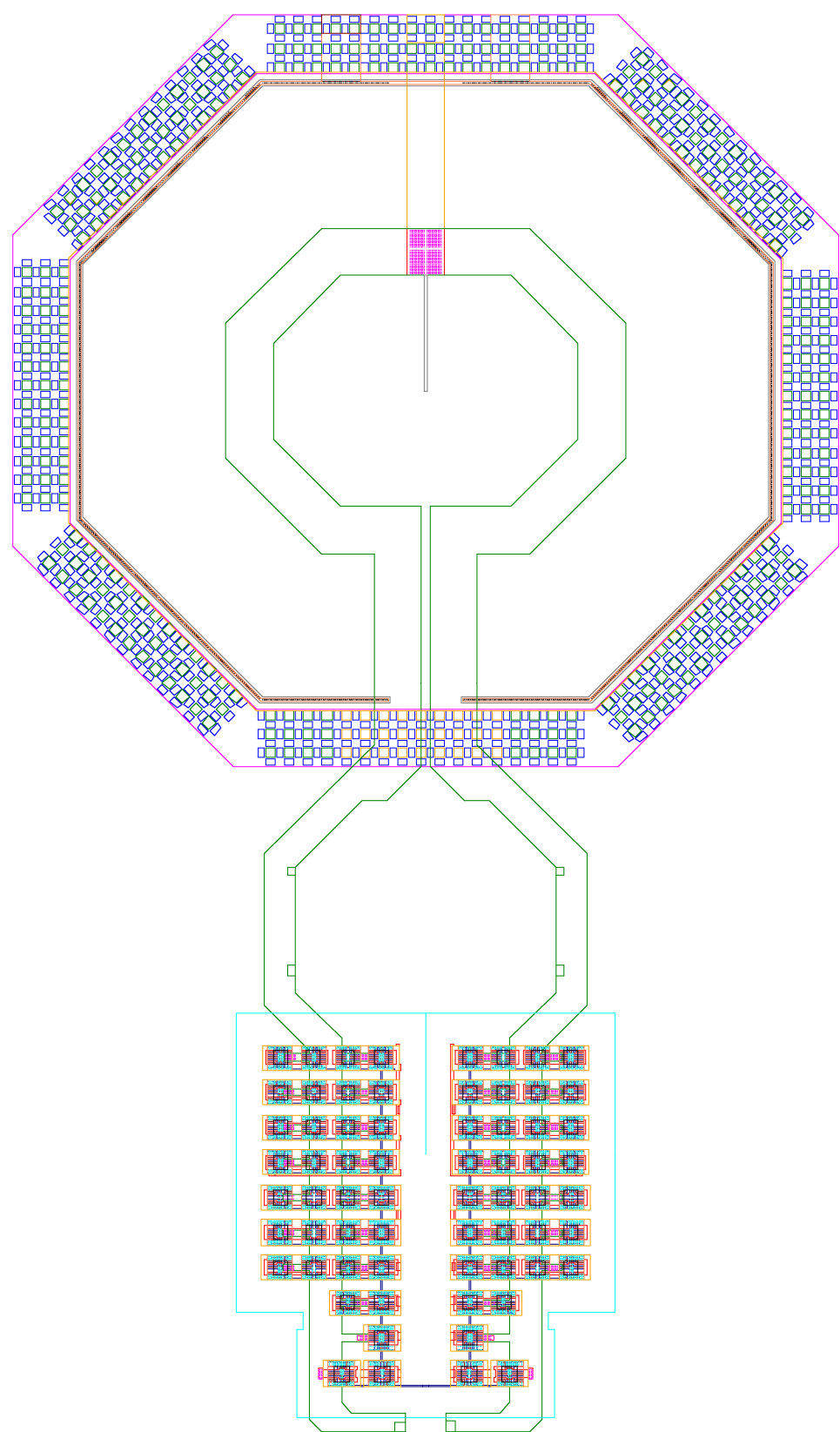
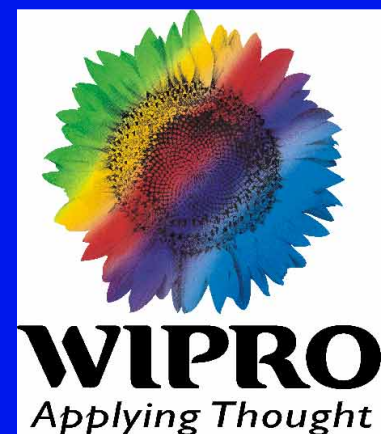
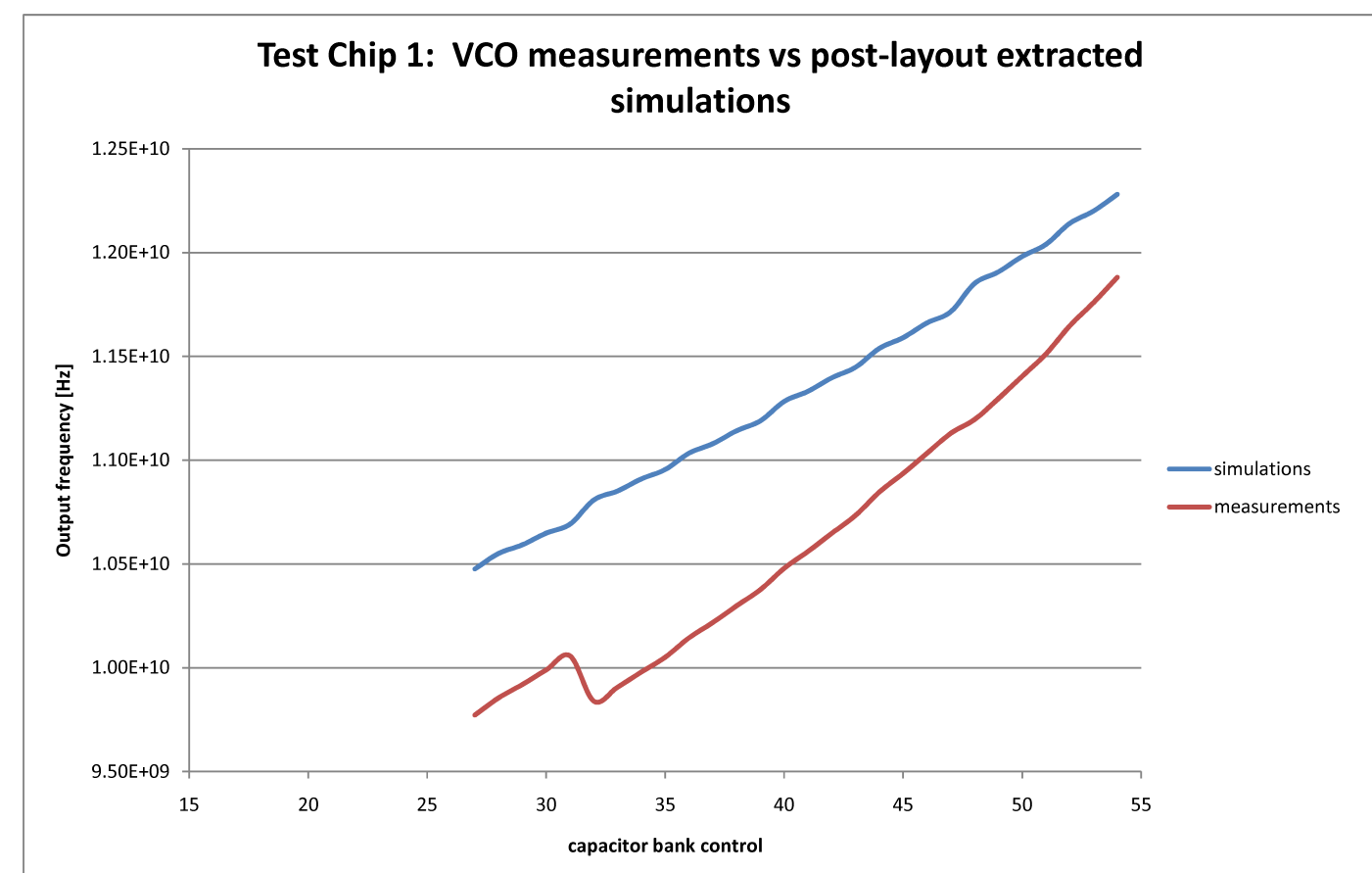


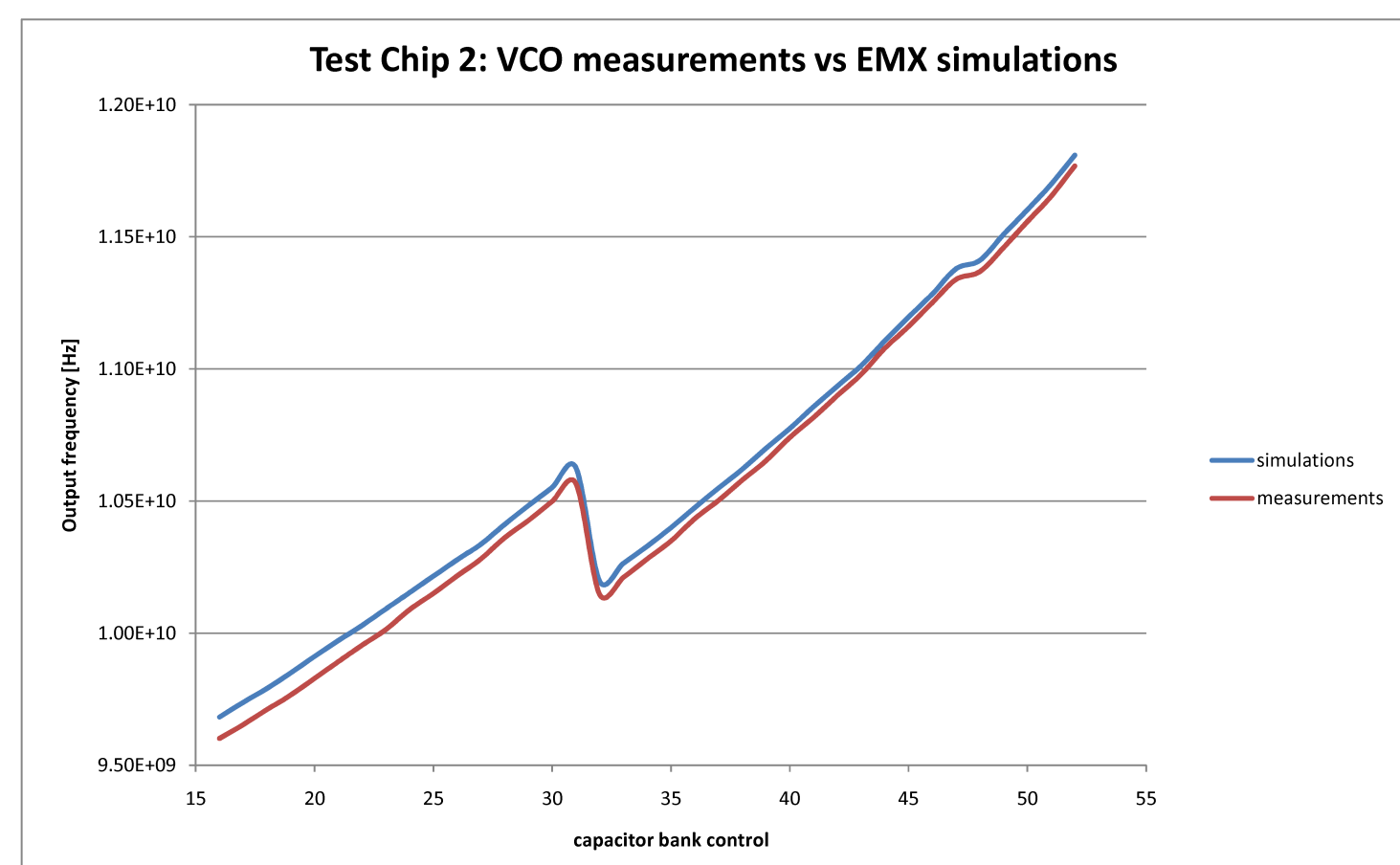
Wipro Testimonial



VCO full-wave extraction, with inductor, capacitor bank and interconnect on a TSMC 90nm process



Before EMX



Using EMX

"If you find it difficult to accept that each time your silicon measurements do not match your EM simulations, no matter how hard you have tried, then we have gone through the same experience! Exhausting electromagnetic extraction times, extremely reduced and simplified extraction elements, poor technology awareness; all of these belong in the past now! Since we have started using EMX, high-frequency chip design is no longer Russian Roulette."

- Dr. Carla Ghidini, NewLogic Technologies, A Wipro Company